

## Memory Design Solution Manual

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Memory Architecture:  $M$  bits Decoders  $M$  bits  $S_0 S_1 \dots S_{M-1}$  Word 0 Word 1 Word 2 Storage cell  $S_1 S_2 \dots S_M$  A 1 Word 0 Word 1 Word 2 Storage cell Word  $N_2$   $2 N$  words  $S_{N_2} \dots S_{2A}$  K 2 1 S Decoder Word  $N_2$  2 Word  $N_2$  1 K 5  $\log_2 N$   $N_2$  1 Word  $N_2$  1 Input-Output ( $M$  bits) Intuitive architecture for  $N \times M$  memory Too many select signals:  $N_d N_{lti} 1 K = \log_2 N$  Decoder reduces the number of select signals Input-Output ( $M$  ...

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In digital system design, the usage of memory elements is unavoidable. In some of the systems, data can be pre-stored in a constant memory. Memory elements can be used as temporary storage of intermediate data. Memory elements can also be used to realize any logic function.

## ~~Memory Design—Digital System Design~~

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